

FIG. 1

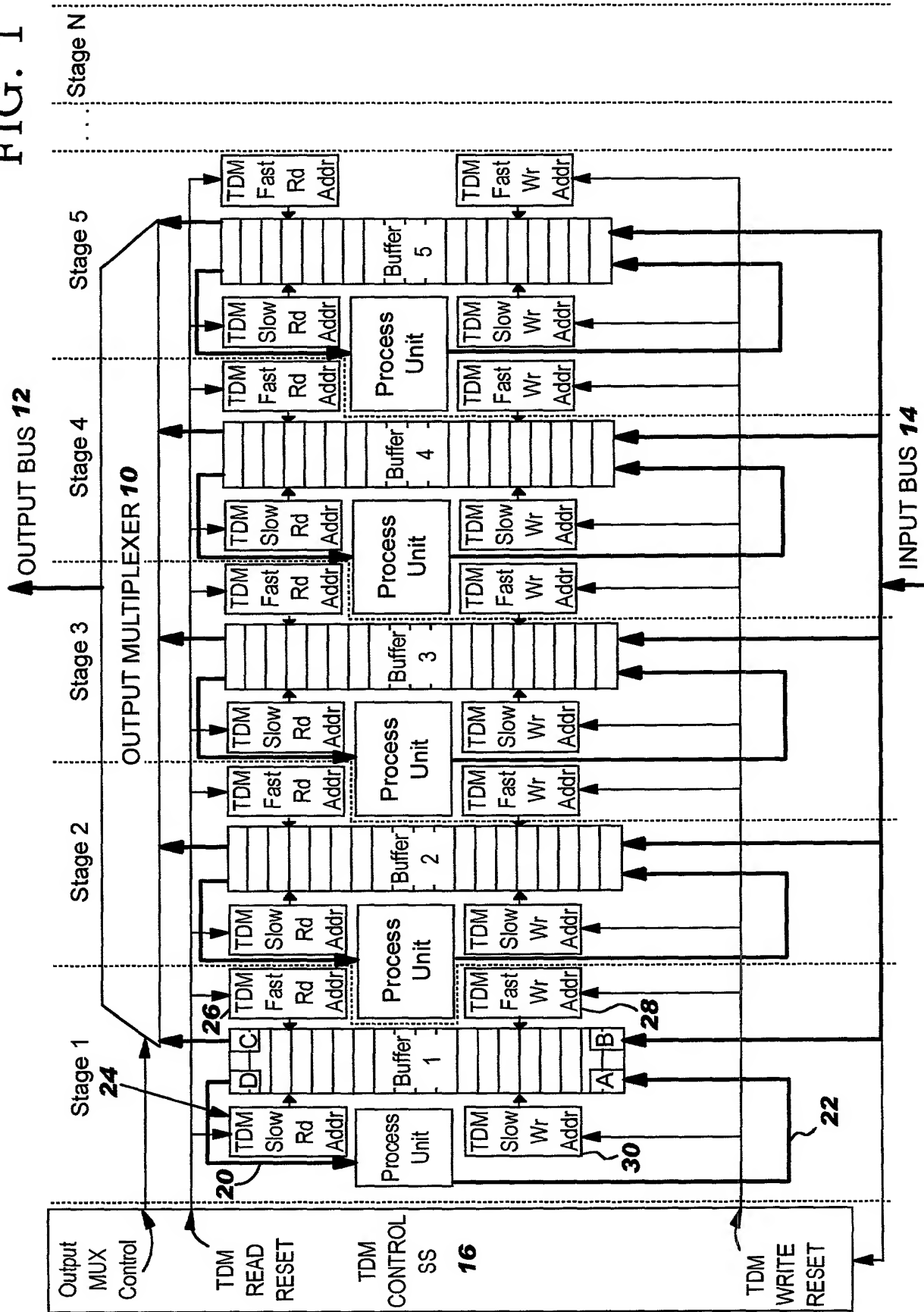
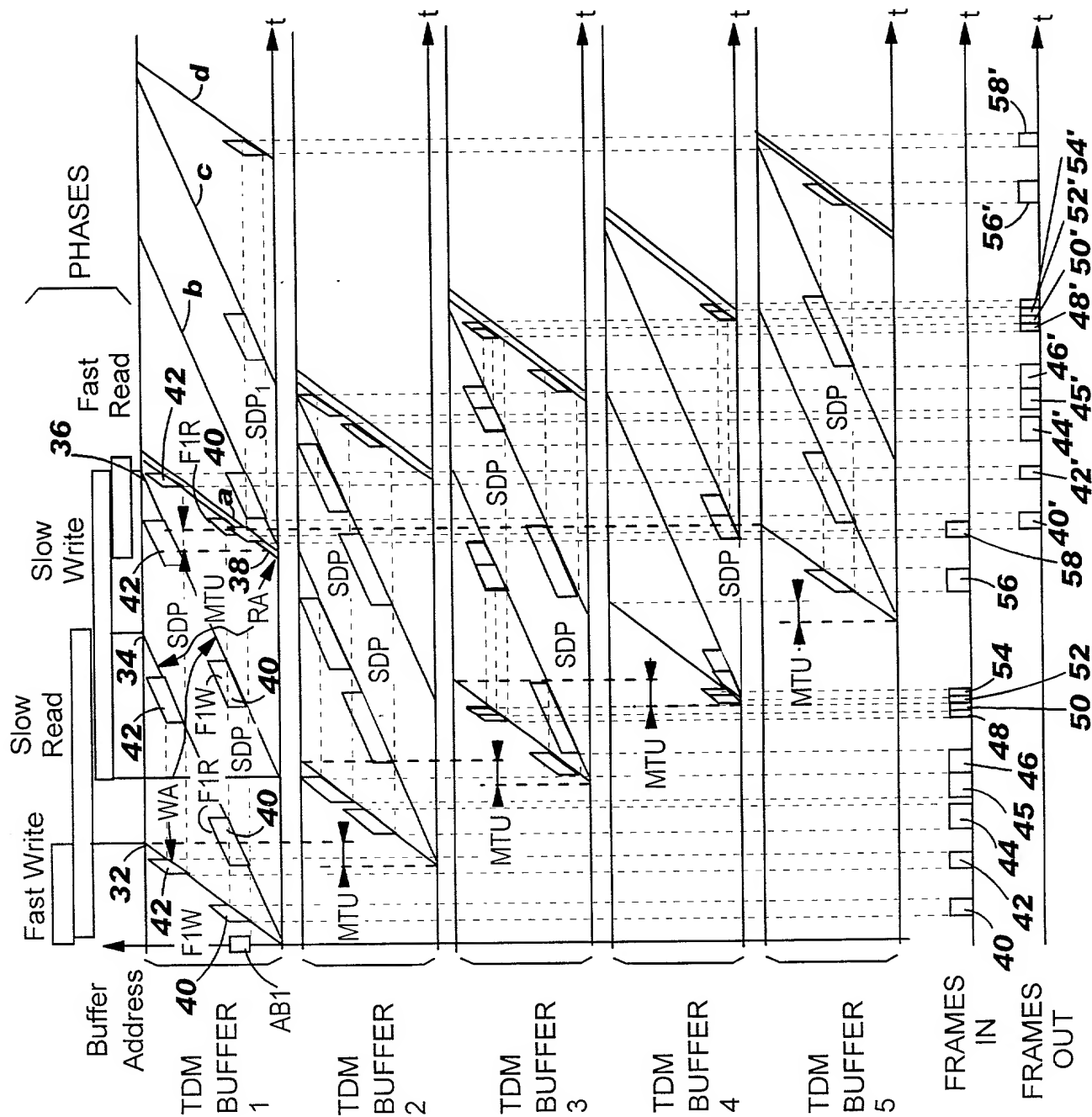


FIG. 2



SDP = Slow Data Process  
WA = Write Addresses  
RA = Read Addresses  
F1R = Frame 1 Read  
F1W = Frame 1 Write  
AB1 = Area in Buffer 1 used by  
Frame 1  
MTU = Maximum Transmitt Unit

FIG. 3

# TDM TEMPLATE

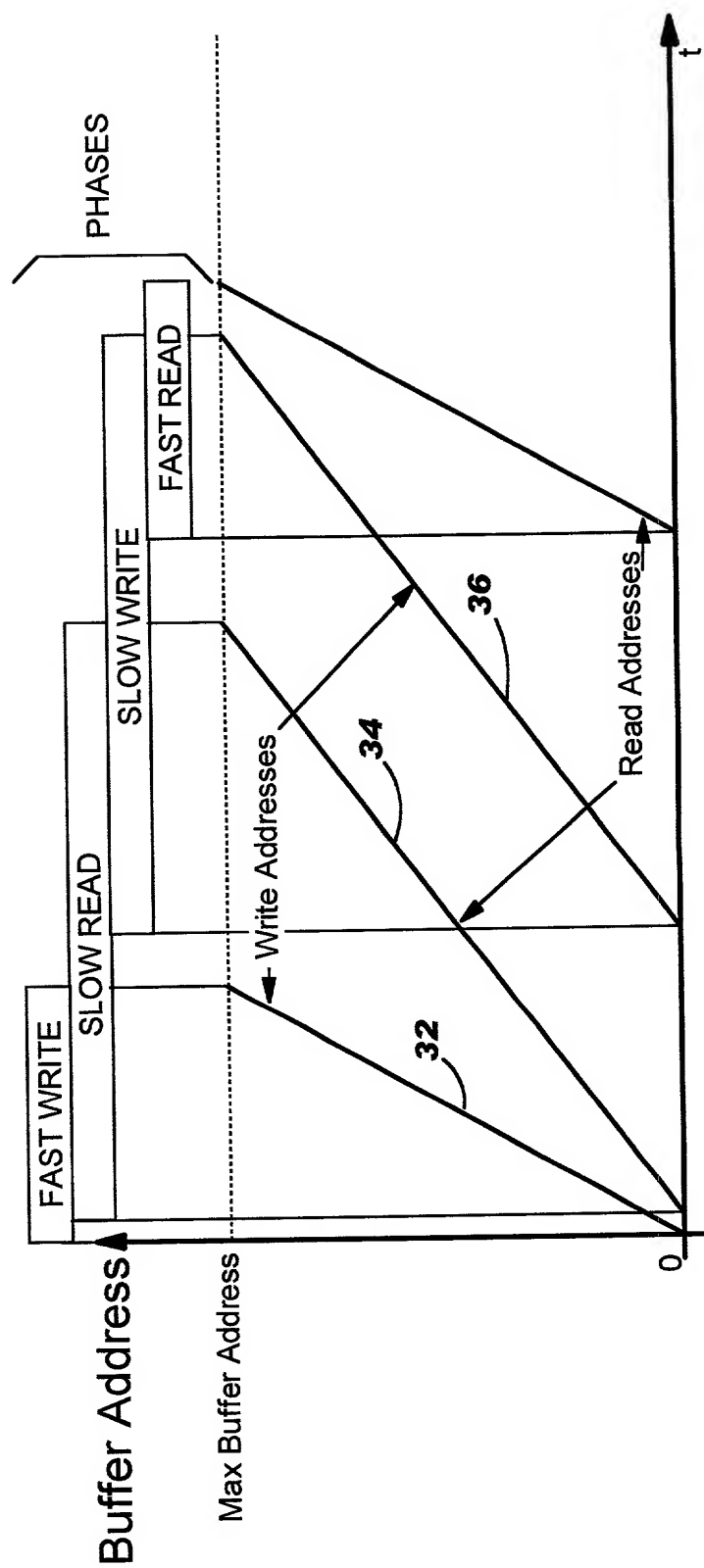


FIG. 4

TDM RD or WRITE ADDRESS GENERATION

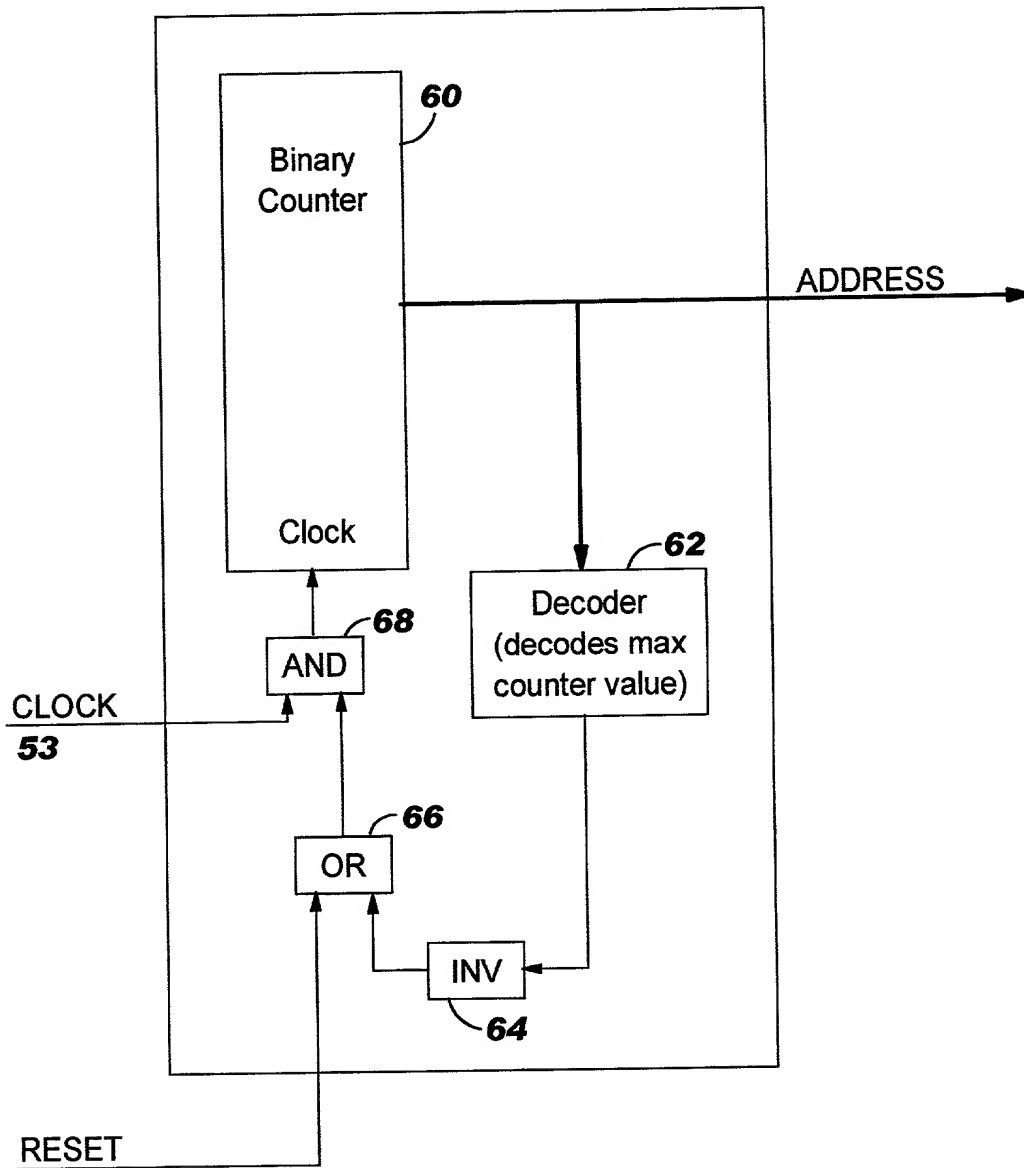


FIG. 5

